

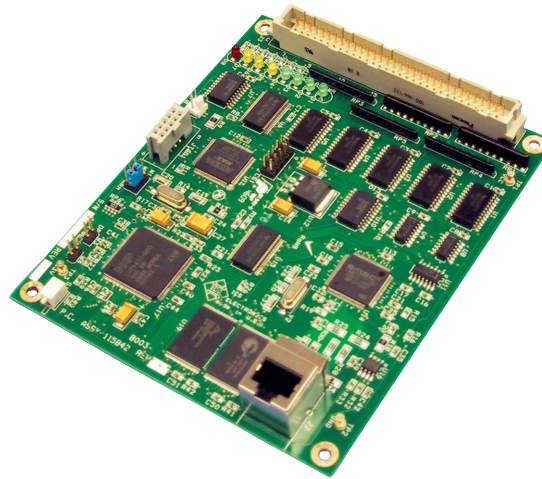
NETWORK INTERFACES

8003 LAN TO PARALLEL DIGITAL INTERFACE

**A flexible interface
between any computer
and devices with
digital signals.**

Description

ICS's Model 8003 Ethernet <-> Parallel Interface is a small, 40 line digital interface that can be used to interface devices with digital signals to the computer via an 10 or 100 Mb Ethernet network. The 8003's high-power TTL level signals can easily drive small relays or other logic elements. In a typical application, the 8003 is located inside the device chassis and is powered by the device's +5 volt power. All digital signal connections are on an upright 96-pin DIN connector at one end of the card. A vertical RJ-45 connector on the other end has the Ethernet signals. A right-angle RJ-45 connector is available on the 8003H. Applications include controlling switching logic, operating displays or passing data to/from other devices.



8003 Interface Card

Versatile Digital Interface

The 8003's digital interface is configured with commands over the network. The configuration commands permit the user to designate the 40 data lines as inputs and/or outputs in 8-bit byte increments, connect bytes into strings, set data polarity, data format, and handshake modes. The user can set bytes as inputs. Input signals are tristated and are pulled up by 33 kohm resistors to 5 Vdc. The inputs are ideal for reading switches, TTL and CMOS signals.

The user can set the output lines to his desired values and save the current configuration in the 8003's Flash memory. The saved configuration becomes the new power-on configuration. At power turn-on, the Digital I/O lines are initially tristated and then set to the saved configuration. A Stable signal is asserted after the digital I/O lines are configured to enable external logic or relays.

Data Transfer Methods

Data transfer capabilities between the computer and the 8003's digital interface depends upon the link to the 8003. When linked to *inst0* the user can access all setup commands and control the digital interface by individual bits, by bytes, or as strings of data values to multiple bytes as shown in Figure 2. When linked to *inst1* data is transparently transferred to the digital interface.

- Bit commands set or reset specific bits in a byte or query a bit's status. New pulse commands let the user pulse single or multiple output lines at the same time.
- Byte commands set all 8 bits in a specific byte or read data from a byte.
- String transfer commands send strings of data characters to one or more output bytes to make a multi-byte output word or read a string of data from one or more input bytes. The user designates these bytes as inputs or outputs when configuring the board.

- Provides a user-definable, 40-line parallel interface with bit, byte, string and binary data transfer capabilities. *Fully configurable to the user's needs.*
- Signal monitor feature allows the 8003 to detect signal changes on 15 inputs. *Reverse channel capability relieves controller of time consuming polling task.*
- High-current drivers and input pullup resistors. *Drives more devices, longer lines and inputs CMOS signals or switch contacts.*
- VXI-11.3 Compatible *Works with the newest test applications and any computer.*
- Device configuration, user's IDN message and IP address stored in nonvolatile Flash memory *User can easily set the power-on configuration.*
- Includes ICS's interactive VXI-11 keyboard program. *Easy control without having to write a program.*
- Interchangeable with ICS's 4803 GPIB and 2303 Serial boards. *Easy interface conversion.*

 RoHS Compliant



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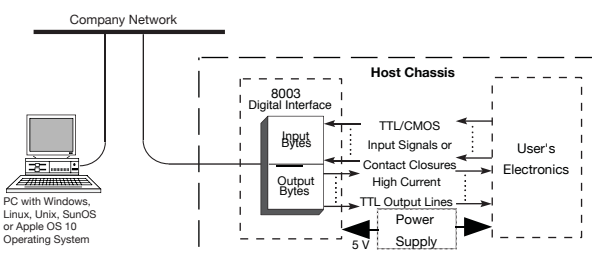


Figure 1 A typical 8003 Application

When linked to *inst1*, the user can send or receive strings of data without commands. The 8003 transparently passes strings of data to user configured output bytes or reads data from user configured input bytes. The data is output or read without having to parse any commands, eliminating the parsing time and speeding up the data transfer. When the binary data format is used, the 8003 can output data at its maximum output rate.

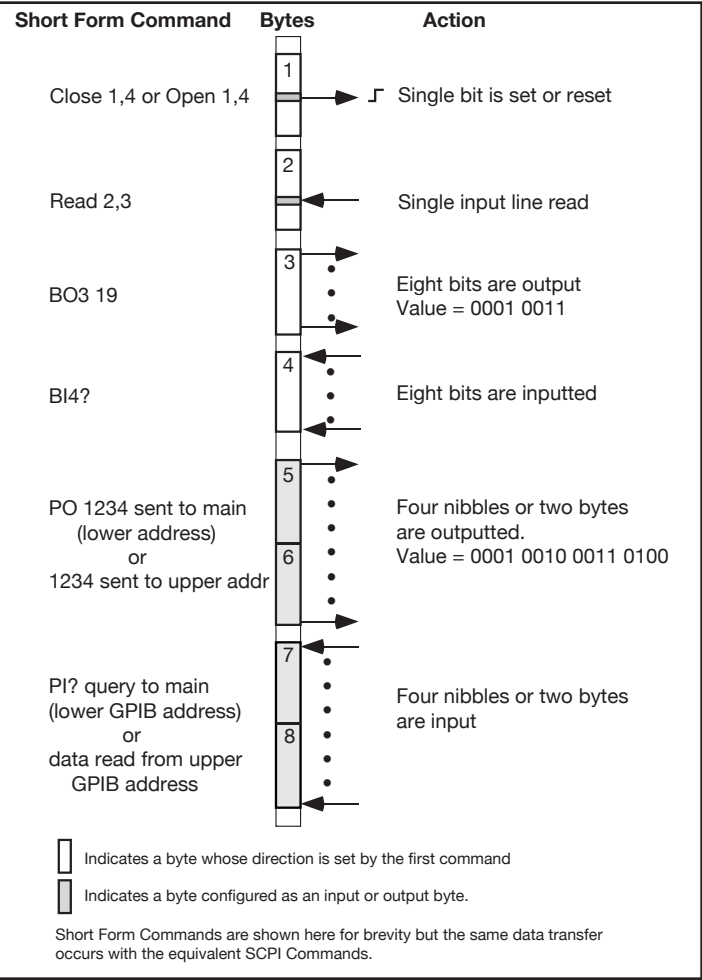


Figure 2 8003 Digital Transfer Methods
 (Figure shows 8 bytes for illustrative purposes. Actual 8003 has 5 bytes)

Outputting Data

The 8003 has three ways to control the digital interface and output data as shown in Figure 2:

- Bit commands set, reset or pulse bits in a specific byte.
- Output byte commands set all bits in a byte and latch an output value (0 to 255) into a specific byte. Data Strobes can be manually generated if needed.

The bit and byte output commands automatically set the bytes to be outputs. The user does not pre configure these bytes with the CONFigure commands.

- Strings of data can be outputted to multiple bytes with a command or transferred transparently. These bytes are preset as string output bytes by the CONFigure command. The 8003 converts the data string characters into bytes, latches the data in the configured output bytes and generates a data strobe pulse to update the external device. The data strings can be a series of decimal values, ACSII HEX characters, or the 0x30-0x3F HEX characters used in ICS's earlier interfaces.

8003 SCPI COMMAND TREE			
SCPI Commands		Short Form Cmds	
SYSTem		System	
:ERRor?			
:VERSion?			
CONFigure		Configure I/O	
[:DIGital]			
:INPut	<channel list>		N
:POLarity	0 1		TPn
:HANDshake	<boolean>		TBn
:OUTput	<channel list>		LN
:POLarity	0 1		LPn
:HANDshake	<boolean>		LH
:CLEar	0 1		C
:EDR	0 1		E
:INHibit	0 1		I
:REMote	0 1		R
:RESet	0 1		
:STRobe	0 1		S
:TRIGger	0 1		TR
:ASTATUS	0 1		A
:BSTATUS	0 1		B
FORMat		Data Format	
:TALK	<ASCIi Hex HEXL Table>		
:TRANSlation	<16 char string>		V
:LISTen	ASCIi Hex HEXL BIN 4833		
ROUTe		Bit Commands	
:CLOSe	byte, bit		CLOSE
:OPEN	byte, bit		OPEN
:RESEt	byte		BRESEt
:PULSe	byte,bit		PL
:CHANnel	number or channel list		P
:WIDTh	10-30000 [50]		PW
SENSe		Input Data	
[:DIGital]			
:DATA			
[:VALue]?			PI?
:PORT?	number or <channel list>		BI?
:PORTn?			BIIn?
:POLarity?			Pn
:RESet:EDR			ER
:BIT?	0-1		READ?
:BYTe?	0-255		BREAD?
[SOURce]		Output Data	
[:DIGital]			
:DATA			
[:VALue]		<string>	PO
:PORTn	0-255		BOIn
:POLarity		0-255	OPn
:STRobe			SP
CALibrate		Calibrate Configuration	
:IDN	string (72 char max		
:DATE	mm/dd/yy		
:DEFault			
:LOCK	1(On) 0(Off) [0]		

Figure 3 8003 SCPI Command Tree

Reading The Input Signals

The 8003 has three ways to read the digital interface lines and input digital data as shown in Figure 2:

- Bit queries read the status of an individual bit from a specific byte.
- Input byte commands read 8 bits of data from a specific byte.
- Strings of data can be read from multiple bytes with a data transfer command or inputted transparently. These bytes are preset as string input bytes by the CONFIGure command. For input strings, the 8003 reads the configured input bytes, converts the data to the selected output format, and outputs it as a string of characters. Data can be inputted with or without handshaking. The input data can be formatted as decimal numbers, as ASCII HEX characters, or into a user selected character set.

Transparent Data Transfer

The user can input and output data without using a string command by linking to the 8003 at *inst1*. Data is read from the configured input bytes each time the 8003 receives a *device_read* rpc. The data is input and formatted just as it would be for reading strings of data.

When outputting data the 8003 converts the data and outputs it to the bytes that the user has previously configured as outputs. Data can be outputted with or without handshaking. The 8003 automatically generates a data strobe each time it loads all of the configured output data bytes. Multiple data words can be transmitted by inserting a comma between data words.

Input Signal Monitoring

The 8003 can monitor up to fifteen lines for signal changes and generate the VXI-11 equivalent of an SRQ to notify the Application program when changes occur. Monitoring is done by setting the 8003's Questionable Transition register to detect positive and/or negative signal transitions and enabling bits in the Questionable Event register. When the enabled bit(s) are detected, the 8003 generates a *device_intr_srq* message (SRQ) to alert the Application to the event. The user's Application program can query the 8003's Questionable Condition Register to determine the input signal states and the Event Register to learn which signal changed state. Application Bulletin AB80-4 describes how to use a Reverse Channel for SRQ notification.

Firmware Customization

The 8003's firmware allows the user to store a custom IDN message and other parameters in the 8003's Flash memory which makes the 8003 appear as part of the OEM's product. The IDN message can be changed to show the OEM's company and product identification including serial number and product revision. A lock function hides the setup variables from the end user and prevents accidental changes to the setup.

The 8003 has an internal WebServer with HTML web pages that the user can customize so the HTML pages match the company's colors and theme. ICS supplies a utility for uploading your HTML pages and graphics to the 8003. These changes let the end user see your company's logo, name, color and control labels which further personalizes the 8003 as part of your product.

Application Bulletin AB80-5 provides guidelines and detailed directions for how the OEM can customize the 8003's web pages.

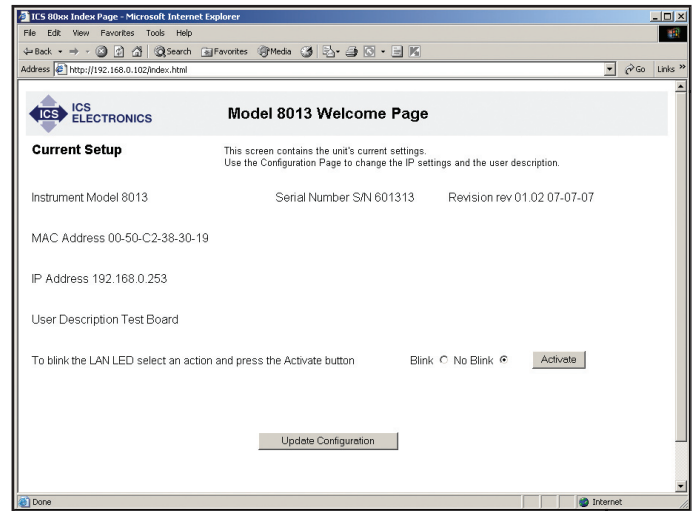


Figure 4 8003 Welcome Page

Easy Programmability

The 8003 can be easily controlled by several programming techniques and languages because it is a VXI-11.3 compliant instrument. If you program with LabVIEW, National Instruments' VISA supports VXI-11.3 instruments. NI's Measurement and Automation Explorer sees the 8003 as a TCP/IP compliant device.

Agilent's VISA library supports VXI-11.3 instruments and the Agilent Connection Manager sees the 8003 as a TCP/IP instrument.

If you are a Visual Basic, VB.Net or C/C++ programmer and work with Windows, you can write your programs to call Agilent's or National Instruments' VISA or Agilent's SICL library.

If you use LINUX or any other flavor of UNIX like SunOS, IBM-AIX, HP-UX, or Apple's OS X, you can communicate with the 8003 through RPC over TCP/IP. RPC (or Remote Procedure Calls) provides an invisible communication medium for the developer. The VXI-11 specification provides an RPCL (Remote Procedure Call Library) that can be used by virtually any operating system to control the 8003.

If you program with Java then you can write a 8003 control program that can be easily moved to many different operating systems. Check ICS's website for a Java application example.

VXI-11 Advantages

VXI-11 is a communication standard developed in conjunction with the VISA Specification. An VXI-11.3 interface like the 8003 can be controlled in Windows systems by programs that make VISA or SICL library calls and in UNIX/LINUX or similar operating systems with RPC calls. The VXI-11 specification provides an RPCL (Remote Procedure Call Library) that can be used by virtually any operating system to control the 8003. While a VXI-11.3 interface like the 8003 will operate in an LXI system, LXI devices are often limited to Windows operating systems with VISA libraries and require IVI drivers.

ICS has developed several Application Notes that describe how to control VXI-11 devices from various computer operating systems and languages. For more information about VXI-11 refer to ICS's VXI-11 Tutorial (Application Bulletin AB80-11) and RPC Programming (AB80-3 and AB80-14) on www.icselect.com/ab_note.

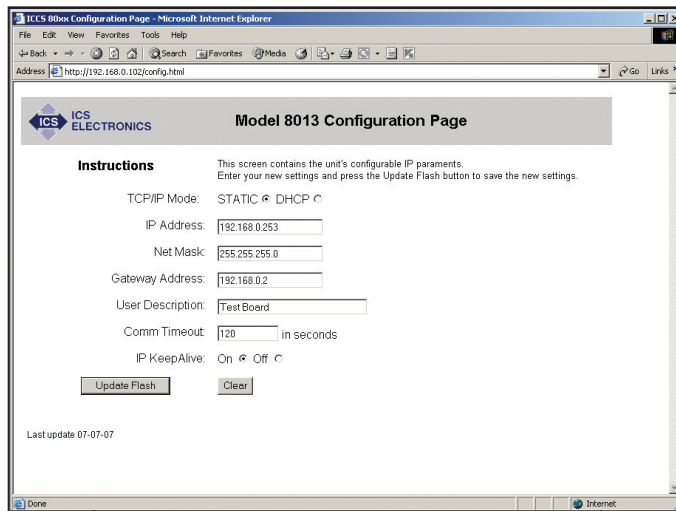


Figure 5 8003 Configuration Page

Easy Network Setup

There are three ways to set the 8003's network settings. The 8003 includes an internal WebServer with HTML web pages that can be accessed by a web browser from any computer. The web pages let the user quickly change the 8003's network settings. Secondly, ICS supplies a windows based configuration utility that walks a user through the network setup process. The third method is to use ICS's defined RPC calls to change the 8003's network settings.

VXI-11 Keyboard Control Program

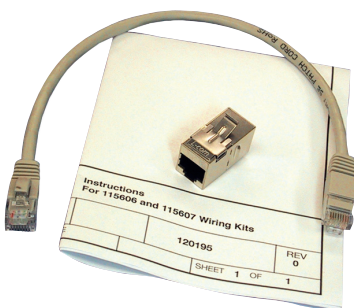
The 8003 includes ICS's VXI-11 Keyboard program for Windows which provides interactive control of VXI-11 instruments from the computer keyboard without having to write a program. The VXI-11 Keyboard program is the ideal utility program for configuring and testing the 8003 or any VXI-11 compatible instrument. Use the VXI-11kybd program to exercise the interface or to try out commands before using them in a program.

8003 Signal Connections

The 8003 has two connectors, an RJ-45 connector for the Ethernet connection and a 96-pin DIN connector with male pins for the digital signals and power. Mating DIN connectors are available with solder eyelet and solder pins or for 64-conductor flat-ribbon cables. The 8003 boards are also available with a DIN connector mounted on the circuit side so the board can be piggybacked on a larger PCB assembly.

The 8003 boards are available with a vertical RJ-45 connector (standard) or with a horizontal RJ-45 connector (8003H version).

Available accessories include mating connectors, Ethernet to rear panel wiring kits, flat-ribbon cable assemblies and the 4803 Relay Driver Board. See the Part Selection Tree on the adjacent page.



ICS Wiring Kit
with 1 foot cable

Mounting

The 8003 has four mounting holes and is designed to be mounted inside the host chassis. Use an ICS Wiring Kit to extend the 8003's Ethernet signal to the rear panel. Each Wiring Kit includes a 1 foot (30 cm) or 2 foot (60 cm) long extension cable and a shielded RJ-45 bulkhead connector for the rear panel.

Relay Driver Board

The 4803DVR Board expands the 8003's drive outputs by providing forty 300 mA relay drivers with convenient screw terminals. A 8003 board with the Digital I/O connector mounted on the circuit side piggybacks on the 4803DVR board to make a compact assembly. An alternate mounting arrangement puts the 4803DVR board on top of a 8003H board. Refer to the 4803DVR board data sheet for more details about the 4803DVR board and mounting dimensions.

The 4803DVR board can be configured to be all relay driver outputs or to have 8 or 16 lines connect directly to ports 1 and 2 of the 8003. Power to the 4803DVR Board is derived from the relay power supply (6 to 32 Vdc). The 4803DVR board has a 5 V regulator and supplies 5 Vdc power to the 8003 which eliminates the need for a separate 8003 power supply.

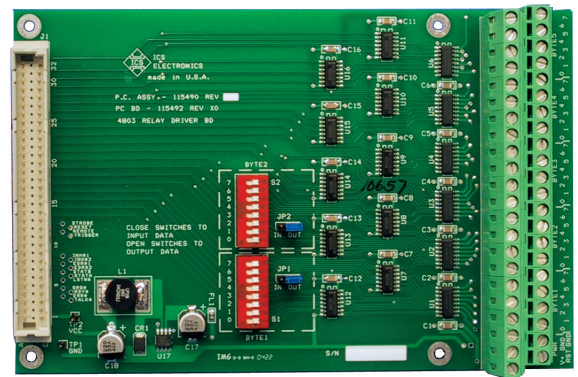


Figure 6 4803DVR Relay Driver Board

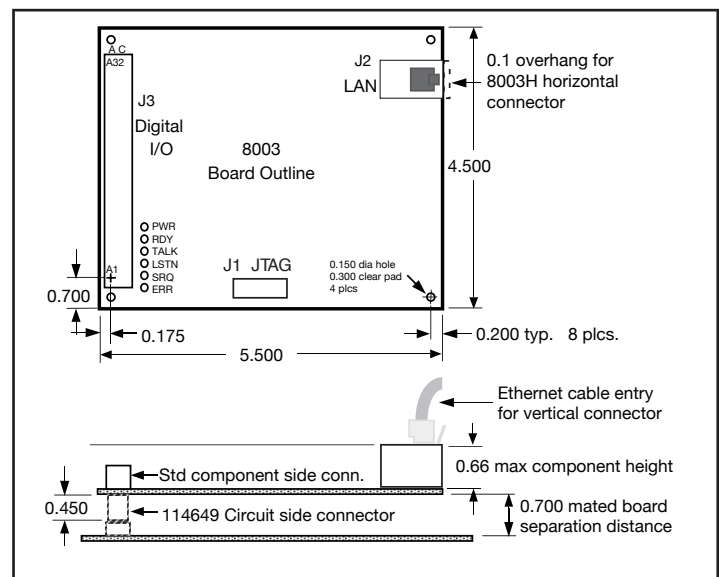


Figure 7 8003 Outline Dimensions

8003: ORDERING GUIDE

Select from one of the two 8003 board styles and then pick your accessory items.

Part Selection Tree		Qty	Part Number
Standard 8003 or 8003H board with the digital IO connector on the component side		(1)	8003 or 8003H
	Select one mating digital IO connector -	(1)	902023
	solder eyelet	(1)	902067
	or	(1)	902124
	flat-ribbon cable		
	Select an Open end, flat ribbon cable assembly, 5 feet long	(1)	112343
	or		
	Select the 4803 Relay Driver Board (For 8003H or 115839 boards)	(1)	115522
	Select a Chassis Wiring Kit - 1 foot long cable	(1)	115606
		(1)	115607
	2 foot long cable		
8003 or 8003H board with the digital IO connector on the circuit side (facing down) for mounting on another PC board		(1)	115848 or 115836
	Select the digital IO connector with solder pins for the mating PC board	(1)	902257
	or		
	Select the 4803 Relay Driver Board (For 115848 or 115849 boards)	(1)	115490
	Select a Chassis Wiring Kit - 1 foot long cable	(1)	115606
		(1)	115607
	2 foot long cable		

ORDERING INFORMATION	Part Number
Ethernet to Parallel Digital Interface Board (Includes Instruction Manual and Support CD)	8003
Ethernet to Parallel Digital Interface Board with standard connector (Board only)	115842
Ethernet to Parallel Digital Interface Board with horizontal RJ-45 connector (Includes Instruction Manual and Support CD)	8003H
Ethernet to Parallel Digital Interface Board with horizontal RJ-45 connector (Board only)	115839
8003 with connector on circuit side (Includes Instruction Manual and Configuration Disk)	115848
8003 with connector on circuit side (Board only)	115849
8003H with connector on circuit side (Includes Instruction Manual and Configuration Disk)	115836
8003H with connector on circuit side (Board only)	115837
Open-end Rainbow colored flat ribbon cable, 5 feet long	112343
4803 Relay Driver Board for 115848 or 115849 boards	115490
4803 Relay Driver Board for 8003, 8003H or 115839 boards	115522
Chassis Wiring Kit with 1 foot long cable	115606
Chassis Wiring Kit with 2 foot long cable	115607

8003: SPECIFICATIONS

Supported Standards

VXI-11 Capabilities

Fully VXI-11.3 compliant

VXI-11.3	Device Interface
Sockets	15 + 1 for UDP
Channel types	Data, Abort and Interrupt
Links	64
Interface Names	inst0 for general use inst1 for transparent data

VXI-11.3 Functions

All VXI-11.3 functions including device read, write, local, remote, clear, trigger, readstb, lock and unlock.

RPC Protocol

Conforms to ONC RPC Version 2, VXI-11

Ethernet Interface

Type	IEEE 802.3 compliant
Speeds	10BaseT (10 Mb/s) 100BaseT (100 Mb/s)
IP Address	Static or DHCP
Factory setting	192.168.0.254 static
Interface name	any [inst0], [inst1]

WebServer Capabilities

Provides the following HTML 4.01 compatible web pages:

Welcome
Configuration
Confirmation
Reboot

IEEE 488.2 Capabilities:

Runs all required 488.2 Common Commands, incorporates an extended IEEE-488.2 Status Reporting Structure and the Message Exchange Protocol.

SCPI Capabilities:

Incorporates the SCPI Command Tree shown in Figure 2. Complies with SCPI version 1994.0

Signal Characteristics

The 8003's parallel I/O signals have the following electrical characteristics. All time delays listed here are maximums, all pulse widths are minimums.

Inputs	40 Digital I/O , 2 Status and Reset Inputs
Input	High = $> +2.0\text{ V} @ \pm 10\text{ }\mu\text{A}$
Logic	Low = $< 0.8\text{ V} @ 250\text{ }\mu\text{A}$
Levels	with 33 Kohm pullup to +5 Vdc for sensing contacts. Max High = 5.5 V

Input Timing	External Data Inhibit line sets within 1 μs of the active edge of the EDR Input signal and resets after data is loaded. Data loading time for 6 BCD/HEX characters is 350 μsec after the 8013 has received a <i>device_read</i> rpc.
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Output	High = $> 3\text{ V}$ with 3 mA source
Logic	High $\Rightarrow > 2\text{ V}$ with 24 mA source
Levels	Low = 0.0 to +0.55 Vdc, 48 mA sink

Output Timing	Data is transferred to the output 2 to 24 ms after receipt of a message depending upon data transfer mode and command. Pulse width 10 to 30000 ms [50]
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Data Stb	Output pulse width, 5 μs .
Trigger	Output pulse width, 5 μs
Remote	Output level asserted when in the remote state
Reset	Output pulse width, 30 μs when 8003 reset or sent *RST command.

Table 2 Digital IO Functions

Input Bytes
Input Signal polarity
Input Handshaking
Talk data format
Talk data conversion table
Output Bytes
Output Polarity
Output Handshaking
Listen data format
Control line polarities
SENSe input polarities
STATus register configurations
SOURCE output polarities

Controls and Indicators

CONTROLS

LAN Rst Board edge push button

LEDs

PWR	Indicates power on
LAN	Unit good and connected to an active network
ACT	Transferring messages to/from the network
RDY	Unit has passed self test
TALK	Unit is addressed to talk
LSTN	Unit is addressed to listen
SRQ	SRQ asserted on GPIB bus
ERR	Blinks for a detected soft error or solid On for a command error

Physical

Size, L x W x H

139.7 x 114.3 x 18.4 mm
(5.5 x 4.5 x 0.725 inches)

Material

PCB	FR406 Fiberglass
Components	RoHS compatible
Construction	Lead Free

Connector and Headers

Digital I/O:	96-pin, 3 row female connector with male pins.
Ethernet:	RJ-45

Temperature

Operation	-10° C to +70° C
Storage	-20° C to +85° C

Humidity

0-90% RH without condensation

Power	+5 \pm 0.2 Vdc @ 350 mA
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Included Accessories

Instruction Manual
Support CD with sample programs
LAN Crossover Cable

Available Accessories

See Ordering Guide on page 5 for a complete list of accessory items.

Mating Solder Eyelet Conn, P/N 902023
Mating Dip Solder Conn, P/N 902067
Mating Flat ribbon Cable Conn, P/N 902124
Wiring Kit w/1 ft cable, P/N 115606
Wiring Kit w/2 ft cable, P/N 115607
Shielded Bulkhead Adapter, P/N 902329
4803 Relay Driver Board, P/N 115490